

安裝Xilinx Vitis在 Ubuntu VM開發機

2023/06



Xilinx Vitis安裝

• Xilinx官網安裝說明

https://docs.xilinx.com/r/2022.1-English/ug1400-vitis-embedded/Installation-Requirements

• Xilinx官網註冊後下載安裝檔案

https://www.xilinx.com/support/download/index.html/content/xilinx/en/downloadNav/vitis/2022-1.html

• Xilinx Unified Installer 2022.1 SFD (TAR/GZIP - 73.81 GB)



Ubuntu VM開發機設置

• 建議最低系統記憶體及CPU







- Ubuntu VM系統碟 50GB
 - Ubuntu 20.04+預設系統資料
- Xilinx Unified Installer 73GB
 - 解壓縮後放置Windows資料夾XilinxVitis再使用分享資料夾與Ubuntu VM共用
 - sudo mount -t vboxsf XilinxVitis /home/kevin/vitis_install (在Ubuntu VM裡執行掛載)

Ubuntu_20.04.4	- 設定		?	×			
一般	共用資料夾						
■ 系統	共用資料夾(F) ―						
 顯示 存放裝置 	名稱 ~ 機器資料夾 XilinxVitis	路徑 D:\DevData\XilinxVitis	存取 自動掛載 At 完 是	<pre>kevin@kevin:~\$ ls vitis_install/Xilinx_Unifi api-ms-win-core-console-l1-1-0.dll api-ms-win-core-datetime-l1-1-0.dll api-ms-win-core-debug-l1-1-0.dll</pre>	<pre>ed_2022.1_0420_0327/ api-ms-win-core-processthreads-l1-1-0.dll api-ms-win-core-processthreads-l1-1-1.dll api-ms-win-core-profile-l1-1-0.dll</pre>	api-ms-win-crt-locale-l1-1-0.dll api-ms-win-crt-math-l1-1-0.dll api-ms-win-crt-multibyte-l1-1-0.dll	msvcp140_1.dll msvcp140_2.dll msvcp140.dll
▲ 音訊	桝態貝科交	🥹 編輯共用 ? X		api-ms-win-core-errorhandling-l1-1-0.dll api-ms-win-core-file-l1-1-0.dll	api-ms-win-core-rtlsupport-l1-1-0.dll api-ms-win-core-string-l1-1-0.dll	api-ms-win-crt-private-l1-1-0.dll api-ms-win-crt-process-l1-1-0.dll	payload scripts
▋ 網路		資料夾路徑: D:\DevData\XilinxVitis ~		api-ms-win-core-file-l1-2-0.dll api-ms-win-core-file-l2-1-0.dll ani-ms-win-core-bandle-l1-1-0.dll	api-ms-win-core-synch-l1-1-0.dl api-ms-win-core-synch-l1-2-0.dl api-ms-win-core-sysipfo-l1-1-0 dl	api-ms-win-crt-runtime-li-i-0.dll api-ms-win-crt-stdio-li-i-0.dll api-ms-win-crt-string-li-i-0.dll	ucrtbase.dll
○ 序列埠 ○ LISB		員科火石構: <u>Allinx Vitis</u> □ 唯讀(R)		api-ms-win-core-heap-ll-1-0.dll api-ms-win-core-interlocked-ll-1-0.dll	api-ms-win-core-timezone-l1-1-0.dll api-ms-win-core-util-l1-1-0.dll	api-ms-win-crt-time-l1-1-0.dll api-ms-win-crt-utility-l1-1-0.dll	vcruntime140_1.dl vcruntime140.dll
一 共用資料夾		✓ 自動掛載(A) 掛載點:		api-ms-win-core-libraryloader-l1-1-0.dll api-ms-win-core-localization-l1-2-0.dll	api-ms-win-crt-conio-l1-1-0.dll api-ms-win-crt-convert-l1-1-0.dll	<mark>bin</mark> concrt140.dll	xsetup xsetup.exe
使用者介面		☑ 設為永久(M)		api-ms-win-core-memory-li-1-0.dll api-ms-win-core-namedpipe-l1-1-0.dll api-ms-win-core-processenvironment-l1-1-0.dl	api-ms-win-crt-environment-i1-1-0.dll api-ms-win-crt-filesystem-l1-1-0.dll api-ms-win-crt-heap-l1-1-0.dl	uata installLibs.sh	
		確定 取消		kevin@kevin:~\$			
			確定取》				



- ・Xilinx Vitis安装碟 250GB
 - •新增第2顆磁碟配置給Ubuntu VM
 - 需在Ubuntu VM裡執行格式化後再掛載到對應目錄
 - 之後Xilinx Vitis安裝可指定安裝到此磁碟對應目錄

😳 Ubuntu_20.04.4	- 設定		? ×
 ● Ubuntu_20.04.4 ■ 一般 ■ 系統 ■ 顧示 ● 存放裝置 ● 音訊 ● 網路 ● 返加増 	- 設定 存放裝置 存放裝置(S) ◆ 控制器: IDE ● 空的 ◆ 控制器: SATA ● ② Ubuntu_20.04.4.vdi ● Ubuntu_20.04.4_ext.vdi	屬性 硬碟(D): SATA 埠 1 □ 固態磁碟 □ 可熱插拔 資訊 類型(格式):標準(VDI) 虛擬大小: 250.00GB 實際大小: 2.00MB 皆細容料, 動態配置的7	? × ▼ 2 機(S) (H)
^{(トック)は} USB		時知貞科: 到怒福(直句) 位置: D:\ISO-VM\L 附加到: 使用金鑰加密:	⊤mm Ibuntu_20.04.4\Ubu····
		確定	取消



- Xilinx Vitis安裝碟 250GB Partition、格式化及掛載目錄
 - 執行lsblk找到250GB對應到/dev/sdb

sdb	8:16	0	250G	6) disk
sr0	11:0	1	1024M	6) rom

- 執行sudo fdisk /dev/sdb -> n (add new partition) -> p (primary) -> 1 (partition num) -> default sector -> w (write table to disk)
- 再執行lsblk確認建立partition sdb1成功

	翌さん				
sdb	8:16	0	250G	0	disk
└─sdb1	8:17	0	250G	0	part
sr0	11:0	1	1024M	0	rom

- 執行格式化sudo mkfs.ext4 /dev/sdb1
- 掛載到目錄
 - sudo mkdir /tools
 - sudo mount /dev/sdb1 /tools
- 使用df確認250GB已掛載到/tools

tmpfs	402020	24	401996	1% /run/user/1000
/dev/sdb1	256980420	61468	243795420	1% /tools



安裝Xilinx Vitis



安裝Xilinx Vitis

- Xilinx Vitis的前置安裝設置
 - sudo apt install libtinfo5 libncurses5 -y
 - sudo cp ~/.Xauthority /root (使用MobaXterm SSH連線到Ubuntu VM並執行sudo ./xsetup前需要 此動作)
- ・安裝Xilinx Vitis
 - 到Vitis安裝目錄,範例目錄為/home/kevin/vitis_install/Xilinx_Unified_2022.1_0420_0327/
 - 執行sudo ./xsetup



Xilinx Unified 2022.1 Inst	aller - Welcome@kevin		-		<
UNIFIED	Welcome				
Xilinx Installer	We are glad you have chosen Xilinx as your platform development products including Vitis, Vivado Design Environment, Lab Edition, Documentation Navigator. Supported operating systems for 2022.1 are: • Red Hat Enterprise Linux 7.4-7.9: 64-bit • Red Hat Enterprise Linux 8.1-8.5: 64-bit (Not supported for Petal • CentOS Linux 7.4-7.9: 64-bit • SUSE Enterprise Linux 12.4, 15.2: 64-bit(Not supported for Petal • Amazon Linux 2 AL2 LTS: 64-bit(Not supported for PetaLinux) • Ubuntu Linux 18.04.1, 18.04.2, 18.04.3, 18.04.4, 18.04.5, 20.04 • A Newer Version Is Available@kevin • Xilinx Design Tools 2022.2 is now available. Click Get Latest to download this latest version and car Click Continue to continue with this installation of Xilinx • Get Latest • Continue separately, with administrative privileges. To reduce installation time, we recommend that you disable any disable any power saving settings of your machine (automatic sloped)	nt partner. This program , Bootgen, HW_Server, F aLinux) aLinux) 4, 20.04.1, 20.04.2 and × incel this installation. < design Tools 2022.1.	20.04.3 LTS: 0 e refer to the ons. Please co and available, o be installed	e Xilinx 54-bit - relevant onfirm before	
E XILINX.					
Copyright © 1986-2023	Xilinx, Inc. All rights reserved.	erences < <u>B</u> ack	Next >	<u>C</u> ance	



Xilinx Unified 2022.1 Installer - Select Product to Install@kevin

Select Product to Install

Select a product to continue installation. You will be able to customize the content in the next page.



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< Back

Next >

Cancel

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Vitis

Installs Vitis Core Development Kit for embedded software and application acceleration development on Xilinx platforms. Vitis installation includes Vivado Design Suite. Users can also install Vitis Model Composer to design for AI Engines and Programmable Logic in MATLAB and Simulink.

Vivado

Includes the full complement of Vivado Design Suite tools for design, including C-based design with Vitis High-Level Synthesis, implementation, verification and device programming. Complete device support, cable driver, and Document Navigator included. Users can also install Vitis Model Composer to design for AI Engines and Programmable Logic in MATLAB and Simulink.

On-Premises Install for Cloud Deployments

Install on-premises version of tools for cloud deployments.

BootGen

Installs Bootgen for creating bootable images targeting Xilinx SoCs and FPGAs.

Lab Edition

Installs only the Xilinx Vivado Lab Edition. This standalone product includes Vivado Design Programmer, Vivado Logic Analyzer and UpdateMEM tools.

O Hardware Server

Installs hardware server and JTAG cable drivers for remote debugging.

PetaLinux

PetaLinux SDK is a Xilinx development tool that contains everything necessary to build, develop, test, and deploy embedded Linux systems.

Documentation Navigator (Standalone)

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Xilinx Unified 2022.1 Installer - Vitis Unified Software Platform@kevin

Vitis Unified Software Platform

Customize your installation by (de)selecting items in the tree below. Moving cursor over selections below provide additional information.

The Vitis unified software platform enables the development of embedded software and accelerated applications on heterogeneous Xilinx platforms including FPGAs, SoCs, and Versal ACAPs. It provides a unified programming model for accelerating Edge, Cloud, and Hybrid computing applications. This installation is a superset that includes the Vivado Design Suite as well. Users can add Vitis Model Composer which is a Xilinx toolbox for MATLAB and Simulink to design for AI Engines and Programmable Logic. If you have been using Xilinx System Generator for DSP, you can continue development using Vitis Model Composer.

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 ✓ Vitis ✓ Vitis ✓ Vitis P Cache (Enable faster on-boarding for new users) ✓ Vitis HLS ✓ Vitis Model Composer(Xilinx Toolbox for MATLAB and Simulink. Includes the functionality of System Generator for DSP) ✓ DocNav ♥ DocNav ♥ Install devices for Alveo and Xilinx edge acceleration platforms ✓ Install devices for Custom Platforms ♥ Devices ♥ UltraScale ♥ UltraScale ♥ Versal ACAP ● Engineering Sample Devices for Custom Platforms ♥ Installation Options NOTE: Cable Drivers are not installed on Linux. Please follow the instructions in UG973 to install Linux cable drivers
Download Size: NA Disk Space Required: 206.71 GB



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stallation Options	Select shortcut and file association options
Select the installation directory	Create program group entries
/tools/Xilinx	Xilinx Design Tools
Installation location(s) /tools/Xilinx/Vitis/2022.1 /tools/Xilinx/Vivado/2022.1 /tools/Xilinx/Vitis_HLS/2022.1 /tools/Xilinx/Model_Composer/2022.1 /tools/Xilinx/DocNav Disk Space Required Download Size: NA Disk Space Required: 206.71 GB Final Disk Usage: 114.7 GB Disk Space Available: 234.66 GB	✓ Create <u>d</u> esktop shortcuts



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Installation Summary UNIFIED Xilinx Installer

Edition: Vitis Unified Software Platform

Devices

- · Install devices for Alveo and Xilinx edge acceleration platforms
- Install Devices for Kria SOMs and Starter Kits
- Devices for Custom Platforms (SoCs, 7 Series, UltraScale, UltraScale+, Versal ACAP)

Design Tools

- Vitis Unified Software Platform (Vitis, Vitis IP Cache (Enable faster on-boarding for new users), Vivado, Vitis HL
- Vitis Model Composer(Xilinx Toolbox for MATLAB and Simulink. Includes the functionality of System Generator for
- DocNav

Installation location

- /tools/Xilinx/Vitis/2022.1
- /tools/Xilinx/Vivado/2022.1
- /tools/Xilinx/Vitis_HLS/2022.1
- /tools/Xilinx/Model_Composer/2022.1
- /tools/Xilinx/DocNav

Disk Space Required

- Download Size: NA
- Disk Space Required: 206.71 GB
- Final Disk Usage: 114.7 GB













安裝Xilinx Vitis

- ・安裝Xilinx Vitis
 - 到目錄/home/kevin/vitis_install/Xilinx_Unified_2022.1_0420_0327/
 - 執行sudo ./ installLibs.sh
 - •加入source /tools/Xilinx/Vitis/2022.1/settings64.sh 到~/.bashrc最後一行



- 設定開機時自動掛載/dev/sdb1到目錄/tools
 - echo '/dev/sdb1 /tools ext4 defaults 0 0' | sudo tee -a /etc/fstab
- 執行sudo reboot
- 安裝完成

